



ONS00317

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: : Date: 9/19/2002

AVERETT, GUY E. ET AL : Group: 2811

APPLN. NO. 10/072145 : Examiner: Thomas Magee

FILED: 2/7/2002 : 703 305 5396

FOR: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW
SUBSTRATE CAPACITANCE

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Declaration
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I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH
THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED
TO: COMMISSIONER OF
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WASHINGTON, D.C. 20231,
ON: September 20, 2002

Date of Deposit

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC

Name of Assignee

Lydia McNamara 9/20/02
SIGNATURE DATE

SUBMITTAL OF SUPPLEMENTAL DECLARATION & POWER OF
ATTORNEY

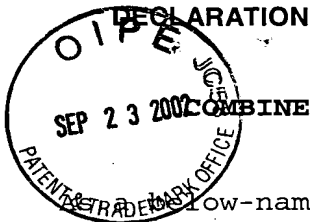
Honorable Commissioner of Patents and Trademarks,
Washington, D.C. 20231
SIR:

Enclosed is a Supplemental Combined Declaration and Power of Attorney
for Patent Application. Please file this document in the above referenced
patent application.

Respectfully submitted,
AVERETT, GUY E. ET AL

James J. Stipanuk
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**COMBINED DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION**

I, the below-named inventor, I hereby declare that:

My residence, post office and citizenship are as stated below except to my name.

I believe I am the original, first and sole inventor (if only name is listed below), or an original, first and joint inventor (if plural names are listed below), of the subject matter which is claimed and for which a patent is sought on the invention entitled:

SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW SUBSTRATE

the specification of which is attached hereto unless the following box is checked:

☒ Application was filed on 2/7/2002
☒ as Application No. 10/072145
 _____ and was amended on _____

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s) Priority Claimed ☐ Yes ☐ No

(Number)

(Country) (Filed)

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below.

Application No.

File date

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of

DECLARATION

Attorney Dkt#ONS00317

this application.

(Application Number) _____ (Filing Date) _____
(Status - patented, pending, abandoned): _____

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith:

James J. Stipanuk, Reg. #44,358
Address all telephone calls to Mr. James J. Stipanuk at
(602) 244-4885; facsimile Number (602) 244-3169.

Address all correspondence to James J. Stipanuk, Semiconductor Components Industries, L.L.C., Patent Administration Dept - MD/A700, P.O. Box 62890, Phoenix, Arizona 85082-2890.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

FULL NAME OF FIRST INVENTOR: **Guy E. Averett**

INVENTOR SIGNATURE: *Guy E. Averett* DATE: 9/6/2002
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CITIZENSHIP: USA
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FULL NAME OF SECOND INVENTOR (if any): **Keith G. Kamekona**

INVENTOR SIGNATURE: *Keith G. Kamekona* DATE: 9/10/2002
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INVENTOR SIGNATURE: *Chandrasekhara Sudhama* DATE: Sept. 18, 2002
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POST OFFICE ADDRESS: Same as above

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FULL NAME OF Fourth INVENTOR (if any): **Weizhong Cai**

INVENTOR SIGNATURE: *Weizhong Cai* DATE: 9/12/02

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CITIZENSHIP: CHINA

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FULL NAME OF Fifth INVENTOR (if any): **Gordon L. Bratten**

INVENTOR SIGNATURE: *Gordon L. Bratten* DATE: 9/19/02

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CITIZENSHIP: USA

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FULL NAME OF Sixth INVENTOR (if any): **Bladimiro Ruiz, Jr.**

INVENTOR SIGNATURE: *Bladimiro Ruiz, Jr.* DATE: 9/13/02

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CITIZENSHIP: USA

POST OFFICE ADDRESS: Same as above
